

ON THE PROPER COORDINATION OF FUSES
WITH SEMICONDUCTOR DEVICES IN THE HEAVY SHORT-CIRCUIT REGION

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INTRODUCTION The maximum permissible current ratings of diodes and thyristors at short-circuits are characterized by manufacturers in the form of the I^2t parameter which is a non-repetitive survival rating for 10 ms overload. Appropriate correction factors are given for current faults of a shorter duration than 10 ms. This interferes with the until quite recently accepted condition for diodes and thyristors at short-circuits of a shorter duration than 10 ms, in which case $I^2t = \text{const.}$ was recommended to be assumed. After subjecting the I^2t parameter to consideration for a shorter duration than 10 ms it may be easily concluded that the condition I^3t or $I^4t = \text{constant}$ would be more suitable.

The manufacturers of thyristors and diodes also determine the value of the I^2t parameter for fault currents having a sinusoidal wave shape. For $i(t) = I_m \sin \omega t$:

$$I^2t = \int_0^{T/2} [i(t)]^2 dt = \frac{1}{2} I_m^2 \left(\frac{T}{2} \right) \quad (1)$$

In view of the changing character of the I^2t/Th versus time it is possible to determine the power x for the $I^x t$ expression, for which it will be constant for the given type of the thyristor/diode on the basis of the following condition:

$$\left(\sqrt{\frac{2(I^2t)_1}{T_1}} \right)^x \cdot \frac{T_1}{2} = \left(\sqrt{\frac{2(I^2t)_2}{T_2}} \right)^x \cdot \frac{T_2}{2}$$

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thus

$$x = 2 \frac{\log \frac{T_1}{T_2}}{\log \left[\frac{(I^2 t)_2 \cdot T_1}{(I^2 t)_1 \cdot T_2} \right]} \quad (2)$$

For instance, in the case of a certain thyristor its manufacturer gave as follows: $(I^2 t)_1 = 1.45 \times 10^6 \text{ A}^2 \text{ s}$ for $T_1/2 = 10^{-2} \text{ s}$ and $(I^2 t)_2 = 0.45 \times 10^6 \text{ A}^2 \text{ s}$ for $T_2/2 = 10^{-3} \text{ s}$. Thus, on the basis of (2) we have:

$$x = 2 \frac{\log \frac{10^{-2}}{10^{-3}}}{\log \frac{0,45 \cdot 10^6}{1,45 \cdot 10^6} \cdot \frac{10^{-2}}{10^{-3}}} = 4,06$$

Therefore, it may be assumed that the dependence $I^4 t = \text{const}$ is justified for $t < 10^{-2} \text{ s}$.

It is possible to determine $I^4 t$ or $I^3 t$ parameter in the function of the $I^2 t$ parameter found for $\frac{T}{2}$ from the following formulae:

for $n = 3$

$$I^3 t = \int_0^{T/2} i^3 dt = \frac{4}{3\pi} I_m^3 \left(\frac{T}{2} \right) \cong 1,19 (I^2 t)^{3/2} \left(\frac{T}{2} \right)^{-1/2} \quad (3)$$

for $n = 4$

$$I^4 t = \int_0^{T/2} i^4 dt = \frac{3}{8} I^4 \frac{T}{2} = 1,5 (I^2 t)^2 \left(\frac{T}{2} \right)^{-1} \quad (4)$$

As to short circuits with heavy duty prospective conditions the damping resistance (Fig.1a) may be neglected for the accretion phase of the current.

Thus,

$$i_{(SC)}(t) = I_m \sin \omega t \quad (5)$$

when only the symmetrical current (SC) component appears, or

$$i_{(ASC)}(t) = I_m (1 - \cos \omega t) \quad (6)$$

at maximum asymmetry and lack of short-circuit damping (ASC = asymmetrical current).

With regard to the two cases of the short-circuit current wave shape further consideration will be given to calculate true current exposures due to the operation of a fuse as expressed in the third or fourth power.

Extensive studies devoted to fuses designed for the protection of diodes and thyristors have shown that:

- the arc ignition moment is practically equal to the moment at which the limited current appears,
- after arc ignition the current remains at an approximately constant value and practically falls down to zero at the moment of natural commutation of the supply voltage.

The course of the current in such a circuit as shown in Fig.1 and under the above given conditions is shown in Fig.2.

The manufacturer of fuses determines the value of the so-called pre-arcing integral $I^2 t = \int_0^{t_p} i_p^2 dt$. In dependence on the symmetrical component of the amplitude of the prospective short-circuit current usually one determines the so-called virtual pre-arcing time

$$t_{vp}^{(2)} = \frac{\int_0^{t_p} i_p^2 dt}{I_m^2} \quad (7)$$

Within the function of that value the true prearcing time t_p was determined for the currents according to (5) and (6) in the form of diagrams in Fig.3a. As to the pre-arcing time, Fig.3b, the relative value has been determined for the limited current in relation to amplitude of the symmetrical short-circuit current. With the application of the curves in Fig.3c value of the virtual arcing time $t_{va}^{(n)}$ have been described to these values for various values of the power exponent for such current, for which the current exposure at fuse operation is intended to be determined. The virtual arcing time

has been determined under the assumption of the relative value of the arc current:

$$k_{ia} = \frac{i_a}{i_l} \quad (8)$$

as follows:

$$t_{va}^{(n)} = \frac{\int_0^{t_a} i_a^n dt}{I_m^n} \cdot \frac{1}{k_{ia}^n} \quad (9)$$

where:

n - power exponent $n = 2, 3, 4$;

(n) - exponent index for the power for which the calculation has been made.

PROPOSED METHOD FOR THE SELECTION OF SHORT-CIRCUIT PROTECTION FOR DIODES AND THYRISTORS

I. To determine for a given type of diode/thyristor the exponent x for the current power in compliance with formula (2) and to round it off to the closest natural number $n = 2, 3$ or 4 .

II. First to select a fuse for a thyristor/diode e.g. in compliance with the till known criterion

$$I^2 t / Th \gg I^2 t / Fuse Total \quad (10)$$

III. To read in the fuse - catalogue the declared value of the Pre-arcing $I^2 t = \int_0^{t_a} i^2 dt$.

IV. To calculate, from the data relating to the short-circuit the maximum value of short-circuit symmetrical current:

$$I_m = \sqrt{2} I_{(RMS)} (prosp. current) \quad (11)$$

V. To calculate, according to formula (7), the virtual pre-arcing time $t_{vp}^{(2)}$ for the fuse.

VI. To take from the diagrams in Fig.3a for the calculated $t_{vp}^{(2)}$ time according to item (V) the real pre-arcing time t_p

for the symmetrical current $t_{p(SC)}^{(2)}$ and the asymmetrical current $t_{p(ASC)}^{(2)}$.

VII. For the t_p as read off in (VI) take the value of the relative limited current $\frac{i_1}{I_m}(SC)$ and $\frac{i_1}{I_m}(ASC)$ from Fig.3b.

VIII. Take the virtual arcing time for $n = 2$: $t_{va(SC)}^{(2)}$ and $t_{va(ASC)}^{(2)}$ from Fig.3c for the values $\frac{i_1}{I_m}$ read according to item (VII).

IX. On the basis of the catalogue data for the fuses, calculate the value:

$$\text{Arcing } I^2t = \int_0^{t_a} i_a^2 dt = \text{Total } I^2t - \text{Pre-arcing } I^2t \quad (12)$$

X. Calculate, for the smaller of the $t_{va}^{(2)}$ values calculated according to (VIII), and the Arcing I^2t value according to (IX), the relative arc current value by transforming the equation (9):

$$k_{ia} = \frac{i_a}{i_l} = \sqrt{\frac{\text{Arcing } I^2t}{t_{va}^{(2)} I_m^2}} \quad (13)$$

XI. According to exponent n from (I), and to $\frac{i_1}{I_m}$ from (VII), read from Fig.3c the value of $t_{va(SC)}^{(n)}$ and $t_{va(ASC)}^{(n)}$

XII. Calculate the value of the arc integral according to criterion $I^n t = \text{const.}$ from the dependence:

$$\text{Arcing } I^n t = \int_{t_p}^{t_a} i_a^n dt = (k_{ia} I_m)^n t_{va}^{(n)} \quad (14)$$

for the bigger of the $t_{va}^{(n)}$ values read off according to (XI).

XIII. For the calculated, according to (V), virtual pre-arcing time t_{vp} for the $I^2t = \text{constant}$ criterion, carry out recalculation for the exponent n as assumed according to (I) in compliance with the curves given in Fig.3d.

XIV. Calculate for the $t_{vp}^{(n)}$ as calculated according to (XIII) the value:

$$\text{Pre-Arcing } I^n t = \int_0^{t_p} i_p^n dt = I_m^n t_{vp}^{(n)} \quad (15)$$

XV. Calculate the fuse total integral on the basis of the dependence:

$$\text{Total } I^n t = \text{Pre-Arc. } I^n t \text{ (acc.to XIV) + Arc. } I^n t \text{ (acc.to XII) (16)}$$

XVI. Recalculate the catalogue value of $I^2 t$ for the thyristor/diode for the n exponent as determined according to (I) in compliance with Fig.4 or according to the dependence (3) or (4).

XVII. Check, if the condition (17) has been complied with:

$$I^n t / I_{Th} \text{ (acc.to XVI)} \stackrel{if}{>} \text{Total } I^n t / I_F \text{ (acc.to XV) (17)}$$

If so, it is safe to assume, that the selected fuse protects the diode/thyristor against the short-circuit current. If the above given condition is not complied with, however, it is necessary either to select a diode/thyristor of a higher warranted value of its $I^2 t$, or a fuse having a smaller total integral, and above all of a lower prearcing integral. In such case the checking cycle according to the above described algorithm is to be repeated.

CONCLUSIONS There is no doubt that the $I^2 t$ parameter is not a constant value for semiconductors in the region of short-circuit current. In addition, it is determined that such wave-shape of short-circuit currents never appears in 50 (60) Hz networks. From the catalogue data for the diode/thyristors it follows that the $I^3 t = \int i_T^3 dt$, or $I^4 t = \int i_T^4 dt = \text{const. value}$ is more true. This makes recalculation necessary of the fuse short-circuits parameters. Suitable dependences and diagrams are given in this paper. The proposed method is more universal than the thus far applied one. It stresses the effect of the short-circuit current value which is more significant than its duration.

In the example given in the appendix it was demonstrated, that a fuse that had been selected in compliance with the classical method does not fulfil the requirements for correct protection according to the proposed method.

APPENDIX. EXAMPLE FOR THE SELECTION OF SHORT-CIRCUIT PROTECTION FOR A THYRISTOR BY A FUSE

Classical method for short-circuit current protection.

Thyristor data: $I_{T(AV)} = 65 \text{ A}$, $I_{T(RMS)} = 100 \text{ A}$

$$(I^2t)_1 = 5.5 \times 10^3 \text{ A}^2\text{s}, \quad T_1/2 = 10^{-2} \text{ s}$$

$$(I^2t)_2 = 4.1 \times 10^3 \text{ A}^2\text{s}, \quad T_2/2 = 3 \times 10^{-3} \text{ s}$$

Fuse data: $I_{F(RMS)} = 100 \text{ A}$

Pre-arcing $I^2t = 10^3 \text{ A}^2\text{s}$, Total $I^2t = 4.6 \times 10^3 \text{ A}^2\text{s}$

The "classical" condition for short-circuit protection the thyristor by the fuse with the above given data is complied with:

$$I^2t/Th = 5.5 \times 10^3 \text{ A}^2\text{s} > I^2t/F_{total} = 4.6 \times 10^3 \text{ A}^2\text{s}$$

Proposed method for short-circuit current protection.

I. From the thyristor catalogue data, (acc. to 2), we have:

$$x = 2 \times \frac{\log \frac{10^{-2}}{3 \times 10^{-3}}}{\log \frac{4.1 \times 10^3}{5.5 \times 10^3} \times \frac{10^{-2}}{3 \times 10^{-3}}} = 2.65$$

Assumed: $n = 3$.

II, III. The following has been assumed in compliance with the previous example: $I_{F(RMS)} = 100 \text{ A}$, Pre-Arcing $I^2t = 10^3 \text{ A}^2\text{s}$; Total $I^2t = 4.6 \times 10^3 \text{ A}^2\text{s}$.

IV. The following has been obtained from the short-circuit data: $I_m = \sqrt{2} \times I_{(RMS)} \text{ (prosp. current)} = \sqrt{2} \times 70 \times 10^3 = 10^5 \text{ A}$.

$$\text{V. } t_{vp}^{(2)} = \frac{\text{Pre-Arcing } I^2t}{I_m^2} = \frac{10^3}{(10^5)^2} = 10^{-7} \text{ s.}$$

VI. For $t_{vp}^{(2)}$ (acc. to V) = 10^{-7} s Fig. 3a $t_p(SC) = 0.13 \times 10^{-3} \text{ s}$;
 $t_p(ASC) = 0.77 \times 10^{-3} \text{ s}$.

VII. For $t_p(SC)$ (acc. to VI) $\xrightarrow{\text{Fig. 3b}}$ $\frac{i_1}{I_m} (SC) = 5 \times 10^{-2}$;
 $\frac{i_1}{I_m} (ASC) = 3 \times 10^{-2}$.

VIII. For $\frac{i_1}{I_m}$ (acc. to VII) $\xrightarrow{\text{Fig. 3c}}$ $t_{va}^{(2)}(SC) = 2 \times 10^{-5}$ s ;

IX. Acc. to (12):
 Arcing $I^2t = \text{Tot. } I^2t - \text{Pre-Arc. } I^2t = 4.6 \times 10^3 - 10^3 = 3.6 \times 10^3 \text{ A}^2\text{s}$

X. Acc. to (13):
 $k_{ia} = \frac{i_a}{i_1} = \frac{\text{Arcing } I^2t}{t_{va}^{(2)} \times I_m^2} = \frac{3.6 \times 10^3}{1.8 \times 10^{-5} \times (10^5)^2} \cong 0.14$

XI. For n (acc. to I) = 3, $\frac{i_1}{I_m}$ (acc. to VII) $\xrightarrow{\text{Fig. 3c}}$
 $t_{va}^{(3)}(SC) = 7 \times 10^{-7}$ s ; $t_{va}^{(3)}(ASC) = 3 \times 10^{-7}$ s .

XII. For k_{ia} (acc. to X), I_m (acc. to IV), $t_{va}^{(3)}(\text{max})$ (acc. to XI)
 we calculate:

Arcing $I^3t = k_{ia} \times I_m^3 \times t_{va}^{(3)}(\text{max}) = 0.14 \times (10^5)^3 \times 7 \times 10^{-7} = 1.9 \times 10^6 \text{ A}^3\text{s}$

XIII. For $t_{vp}^{(2)}$ (acc. to V), and n (acc. to I) $\xrightarrow{\text{Fig. 3d}}$
 $t_{vp}^{(3)}(SC) = 7 \times 10^{-9}$ s ; $t_{vp}^{(3)}(ASC) = 4.8 \times 10^{-9}$ s .

XIV. For $t_{vp}^{(3)}(\text{max})$ (acc. to XIII); I_m (acc. to V) and n (acc. to I)
 we calculate:

Pre-Arcing $I^3t = t_{vp}^{(3)}(\text{max}) \times I_m^3 = 7 \times 10^{-9} \times (10^5)^3 = 7 \times 10^6 \text{ A}^3\text{s}$

XV. For Pre-Arcing I^3t (acc. to XIV) and Arcing I^3t (acc. to XII)
 we calculate:

Total $I^3t = \text{Pre-Arcing } I^3t + \text{Arcing } I^3t = 7 \times 10^6 + 1.9 \times 10^6 =$
 $= 8.9 \times 10^6 \text{ A}^3\text{s} .$

XVI. For n (acc. to I), and the value of the thyristor I^2t (acc.
 to I) $\xrightarrow{\text{Fig. 4}}$ $I^3t/Th = 5 \times 10^6 \text{ A}^3\text{s}$

XVII. The condition (17) has not been complied with:
 I^3t/Th (acc. to XVI) = $5 \times 10^6 \text{ A}^3\text{s} \overset{\text{NO!}}{>} \text{Total } I^3t_{\text{Fuse}}$ (acc. to XV) =
 $= 8.9 \times 10^6 \text{ A}^3\text{s}$

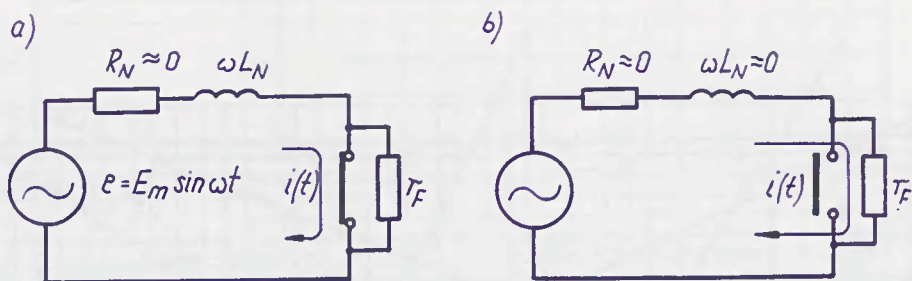


Fig. 1. Substitute diagram for a short-circuit with a fuse operating: a) in the prearcing time, b) during the arc burning time.

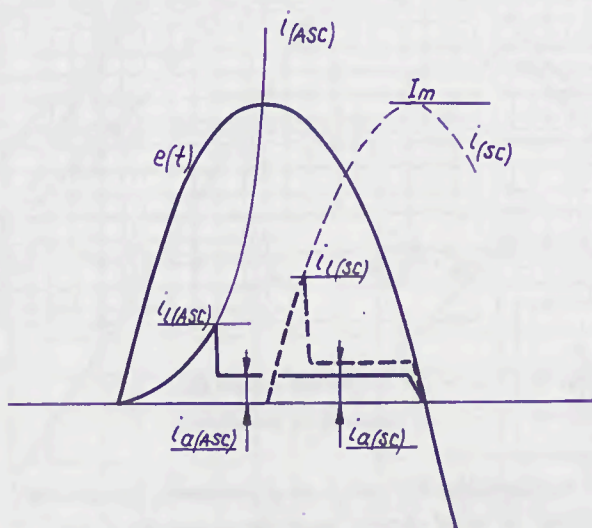


Fig. 2. Short-circuit current limited by a fuse:
 $i(sc)$ - symmetrical current, $i(ASC)$ - asymmetrical current.

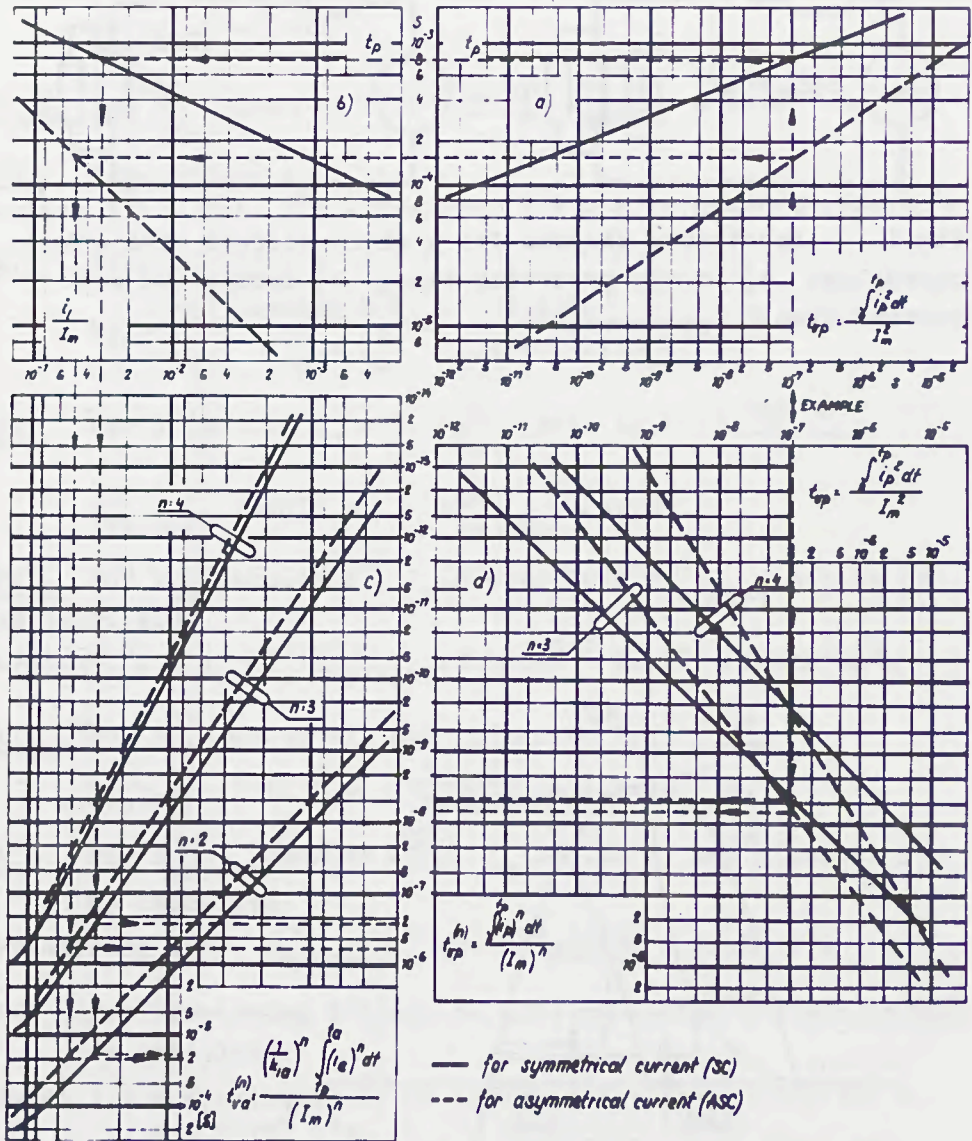


Fig.3. Diagrams for recalculating the values of characteristic parameters of a fuse operating at short-circuits under the assumption of the $I^n t = \int i^n dt$

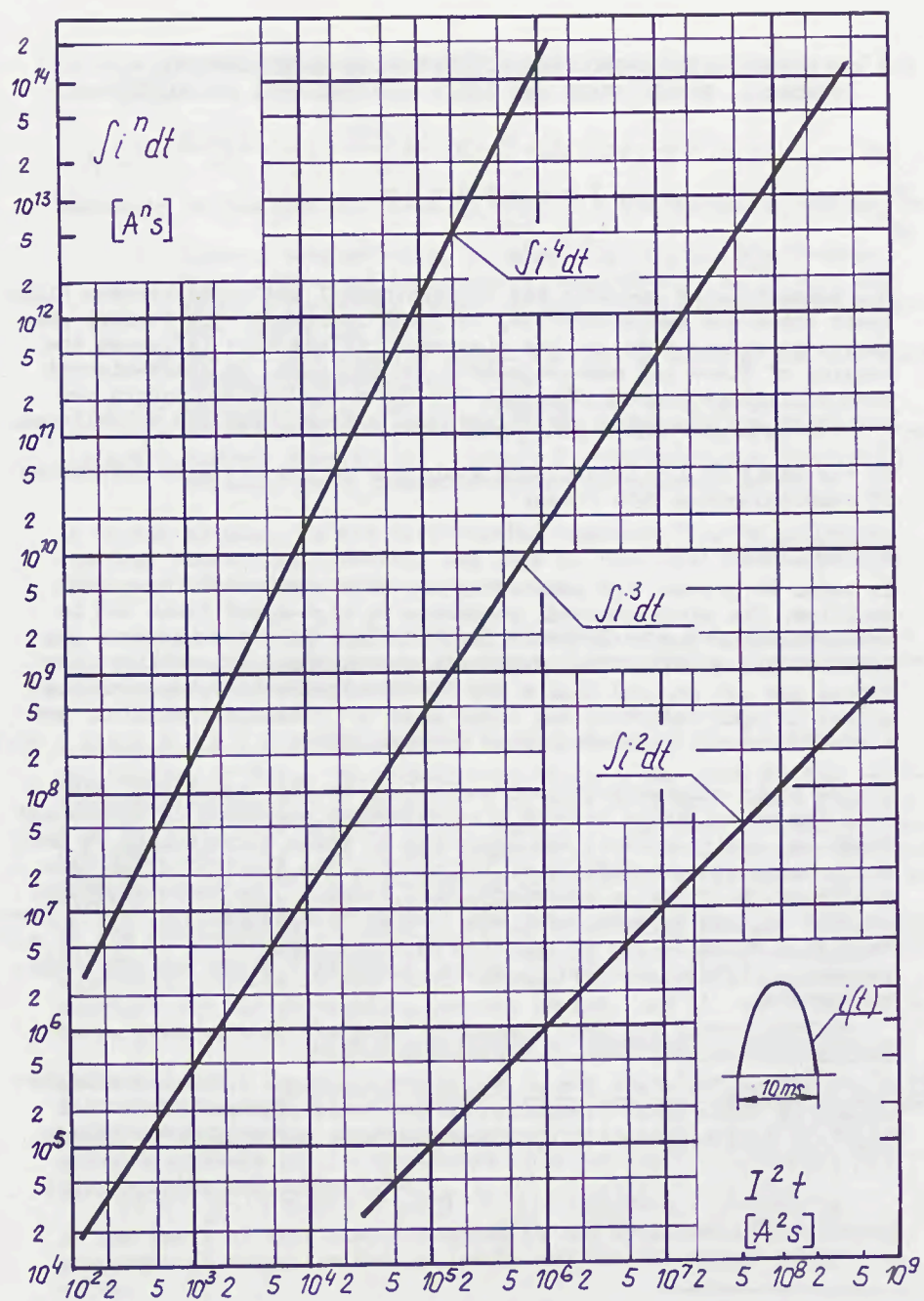


Fig.4. Dependence of the $\int i^n dt = \int (I_m \sin \omega t)^n dt$ versus the value of the $\int i^2 dt$ for $T/2 = 10^{-2}$ s.